

XRT Timeline to be uploaded on 2016/12/15

Period: 2016/12/15 10:02:00 - 2016/12/20 10:30:00

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Normal mode

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XOB #1B3F: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (2x2,1ms) - Leak (2x2,1ms) - 90s cad (G-band/Leak first)											
Term	Pointing (x, y)		Comment								
12/15 10:15:00 - 12/15 12:29:54	Track (-308.6, -36.5) @ 12/15 10:12:00		# OP start + 10min, EIS QS spectral atlas.								
PROG= 09 Inf.-time(s)											
└─ Subr= 1 1-time(s) 2.0sec											
└─ Seqn= 26 1-time(s) 2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
└─ Subr= 2 40-time(s) 90.0sec											
└─ Seqn= 8 1-time(s) 2.0sec											
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3 0 2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2 0 2.0sec
└─ Seqn= 6 1-time(s) 2.0sec											
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3 0 2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

XOB #1AFB: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with											
Term	Pointing (x, y)		Comment								
12/15 12:33:00 - 12/15 15:59:54	Track (-697.3, 156.7) @ 12/15 12:30:00		AR observation								
12/15 16:13:00 - 12/15 19:34:30	Track (-673.2, 157.4) @ 12/15 16:10:00		# AR observation								
PROG= 04 Inf.-time(s)											
└─ Subr= 1 1-time(s) 2.0sec											
└─ Seqn= 56 1-time(s) 2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0 0 2.0sec
└─ Subr= 2 4-time(s) 2.0sec											
└─ Seqn= 75 1-time(s) 2.0sec											
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2 0 2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2 0 2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 2.0sec
└─ Seqn= 23 4-time(s) 240.0sec											
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1 0 2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1 0 75.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1 1 2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1 1 75.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1 2 2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1 2 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

XOB #1B14: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T											
Term	Pointing (x, y)		Comment								
12/15 16:03:00 - 12/15 16:09:54	Fixed (0.0, 0.0)		synoptic, shifted a few hours.								
12/16 16:03:00 - 12/16 16:09:54	Fixed (0.0, 0.0)		synoptic								
12/17 06:11:00 - 12/17 06:17:54	Fixed (0.0, 0.0)		synoptic, shifted 8.0 min								
PROG= 14 1-time(s)											
└─ Subr= 1 1-time(s) 2.0sec											
└─ Seqn= 5 1-time(s) 2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0 0 2.0sec
└─ Seqn= 1 1-time(s) 2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
└─ Seqn= 99 1-time(s) 2.0sec											
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
└─ Seqn= 67 1-time(s) 2.0sec											
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
└─ Seqn= 54 1-time(s) 2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

XOB #1B29: AR Monitoring (HOP306) - Al-poly, 384x384, 60s cad, with G-band (3ms/ 3ms VLS=CLS)													
Term		Pointing (x, y)				Comment							
12/15 20:13:00 - 12/16 01:50:00		Track (-684.9, 170.0) @ 12/15 20:10:00				# HOP 306 with IRIS							
PROG= 06 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 56 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 16 60-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	15.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	15.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	15.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	3	15.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B01: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with													
Term		Pointing (x, y)				Comment							
12/16 02:13:06 - 12/16 06:05:54		Track (-604.5, 159.2) @ 12/16 02:00:00				AR observation							
12/16 07:11:06 - 12/16 11:59:54		Track (-570.4, 160.0) @ 12/16 06:36:00				# AR cont.							
12/16 20:37:06 - 12/17 06:07:54		Track (-463.5, 162.5) @ 12/16 20:10:00				# AR cont.							
PROG= 03 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 56 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 4-time(s) 2.0sec													
Seqn= 75 1-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 51 4-time(s) 300.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B15: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(512/4096), Med-Al													
Term		Pointing (x, y)				Comment							
12/16 06:09:00 - 12/16 06:35:30		Fixed (0.0, 0.0)				synoptic, shifted 6.0 min and extended to 30 mins.							
PROG= 18 1-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 1 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 46 2-time(s) 2.0sec													
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 58 2-time(s) 2.0sec													
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 71 2-time(s) 2.0sec													
	med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

med-AI/Open	med-AI/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B5B: QS (Filter-Ratio with AI/poly and AI/mesh), 384FOV at 1064, 1048 with G-band (3ms/3ms leak) 30s cad - AEC0- HOP328-2

Term	Pointing (x, y)	Comment
12/16 12:03:00 - 12/16 15:59:54	Track (198.3, 213.8) @ 12/16 12:00:00	HOP328 with ALMA
12/16 16:13:00 - 12/16 20:09:30	Track (-29.9, -33.0) @ 12/16 16:10:00	HOP 328 with ALMA
PROG= 20 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 56 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-AI close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 37 30-time(s) 30.0sec		
Open/AI-mesh	Open/thick-AI close	Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=95 0 0 2.0sec
AI-poly/Open	AI-poly/thick-AI close	Safe Norm 8.00s Obs 1x1 384x384 (1064, 1048) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1AFE: AR - Standard Core - (Filter-Ratio with AI/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and AI/poly context, with

Term	Pointing (x, y)	Comment
12/17 06:21:00 - 12/17 10:32:30	Track (-378.3, 164.2) @ 12/17 06:18:00	# AR cont.
PROG= 08 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 56 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-AI close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2 5-time(s) 2.0sec		
Seqn= 75 1-time(s) 2.0sec		
AI-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
AI-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 53 4-time(s) 180.0sec		
AI-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 55.0sec
AI-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 55.0sec
AI-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, AI/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-AI,thick-Be -384x384 + AI-poly 512

Term	Pointing (x, y)	Comment
12/15 10:15:00 - 12/15 12:29:54	Track (-308.6, -36.5) @ 12/15 10:12:00	# OP start + 10min, EIS QS spectral atlas.
12/15 12:33:00 - 12/15 15:59:54	Track (-697.3, 156.7) @ 12/15 12:30:00	AR observation
12/15 16:13:00 - 12/15 19:34:30	Track (-673.2, 157.4) @ 12/15 16:10:00	# AR observation
12/15 20:13:00 - 12/16 01:50:00	Track (-684.9, 170.0) @ 12/15 20:10:00	# HOP 306 with IRIS
12/16 02:13:06 - 12/16 06:05:54	Track (-604.5, 159.2) @ 12/16 02:00:00	AR observation
12/16 07:11:06 - 12/16 11:59:54	Track (-570.4, 160.0) @ 12/16 06:36:00	# AR cont.
12/16 12:03:00 - 12/16 15:59:54	Track (198.3, 213.8) @ 12/16 12:00:00	HOP328 with ALMA
12/16 16:13:00 - 12/16 20:09:30	Track (-29.9, -33.0) @ 12/16 16:10:00	HOP 328 with ALMA
12/16 20:37:06 - 12/17 06:07:54	Track (-463.5, 162.5) @ 12/16 20:10:00	# AR cont.
12/17 06:21:00 - 12/17 10:32:30	Track (-378.3, 164.2) @ 12/17 06:18:00	# AR cont.
PROG= 07 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
AI-poly/Open	AI-poly/thick-AI close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn=100 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-AI close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-AI	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-AI/Open	med-AI/thick-AI close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
AI-poly/Open	AI-poly/thick-AI close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 84 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-AI	Open/thick-AI close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-AI	Open/thick-AI close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * *

Active Region Search

* * * * *

NOT USED

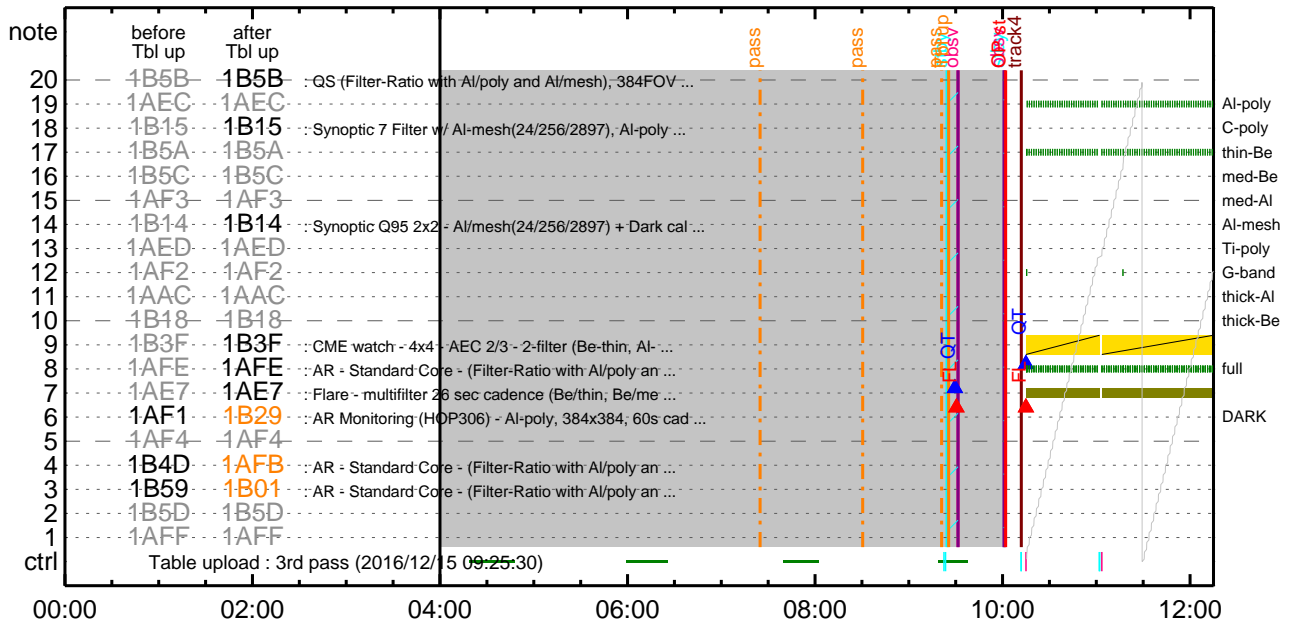
* * * * *

Flare Detection

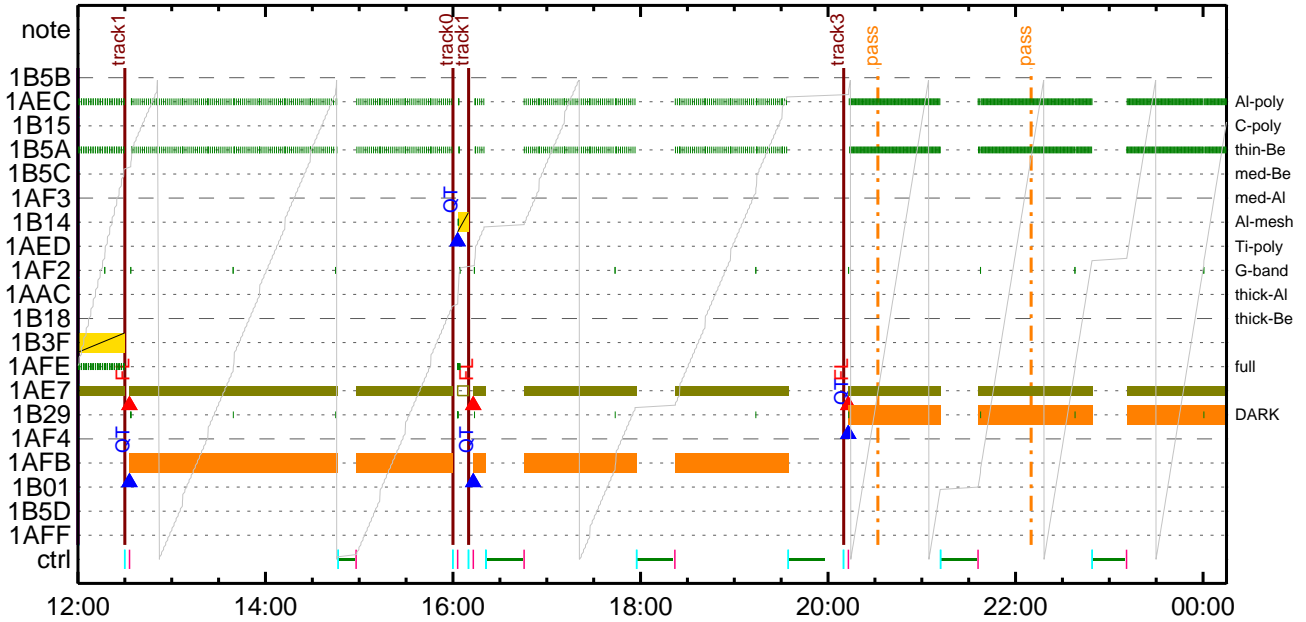
* * * * *

FLD Patrol											
Term		Pointing (x, y)						Comment			
12/15 16:10:18 - 12/16 06:06:18		Track (-673.2, 157.4) ^{@ 12/15 16:10:00}						# AR observation			
12/16 07:08:24 - 12/16 16:00:18		Track (-570.4, 160.0) ^{@ 12/16 06:36:00}						# AR cont.			
12/16 16:10:18 - 12/17 06:08:18		Track (-29.9, -33.0) ^{@ 12/16 16:10:00}						HOP 328 with ALMA			
12/17 06:18:18 - 12/20 10:30:00		Track (-378.3, 164.2) ^{@ 12/17 06:18:00}						# AR cont.			
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

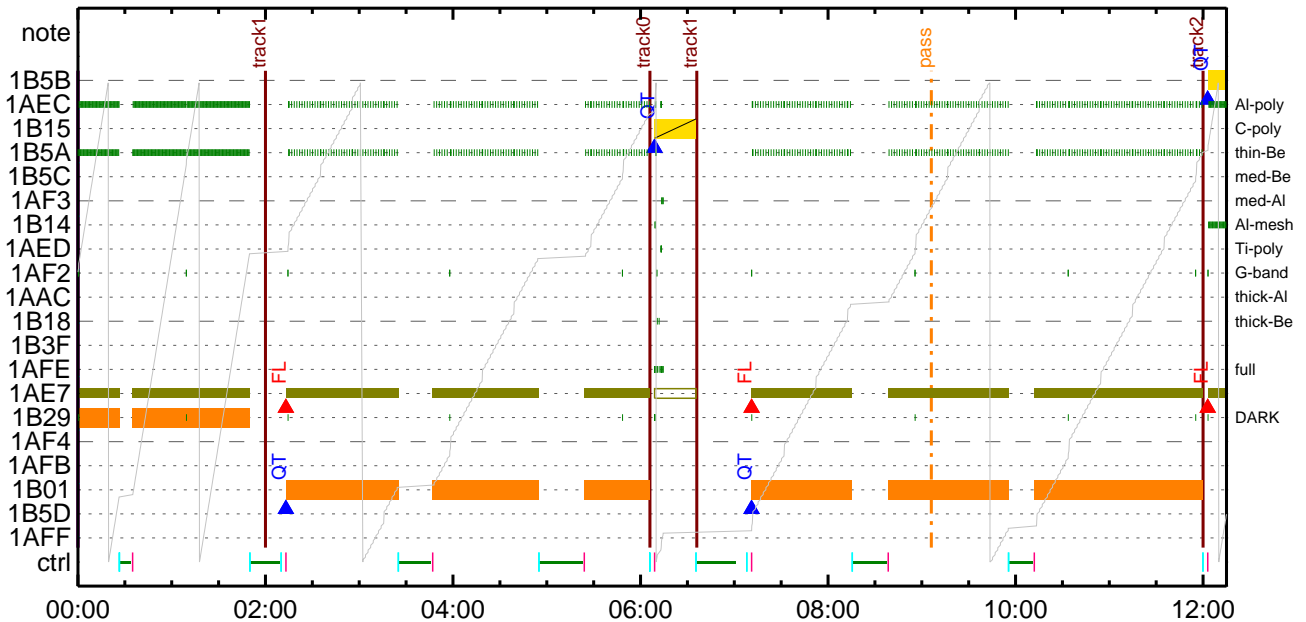
CMDI #0390 2016/12/15



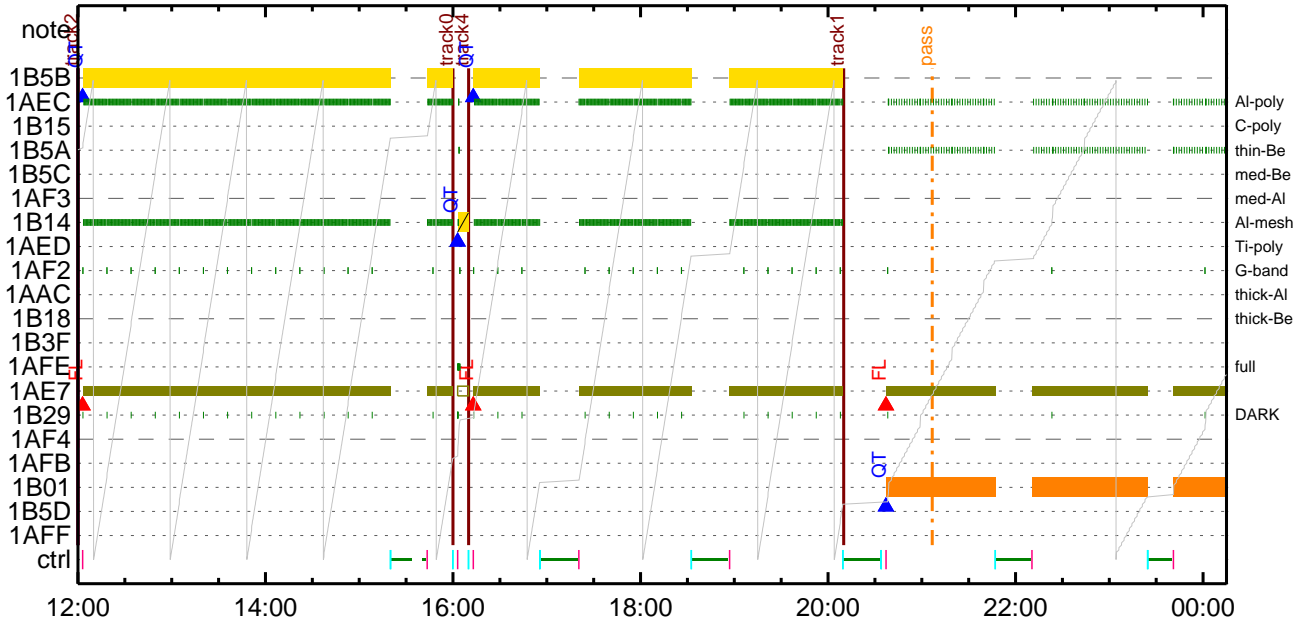
CMDI #0390 2016/12/15



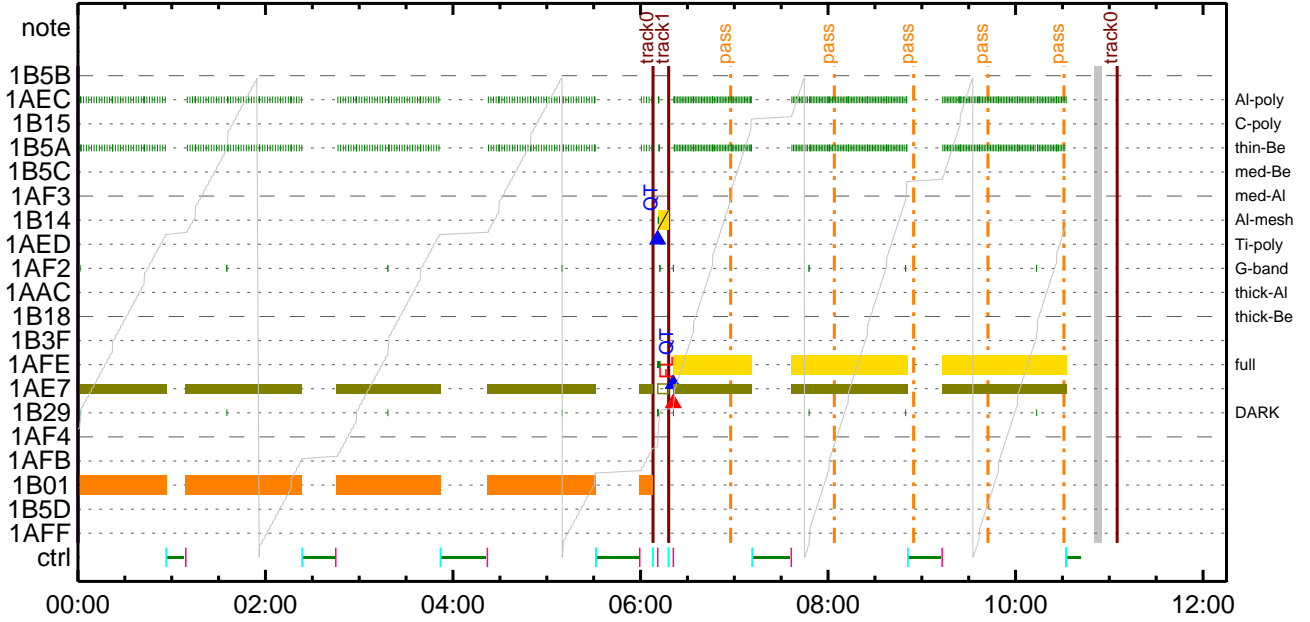
CMDI #0390 2016/12/16



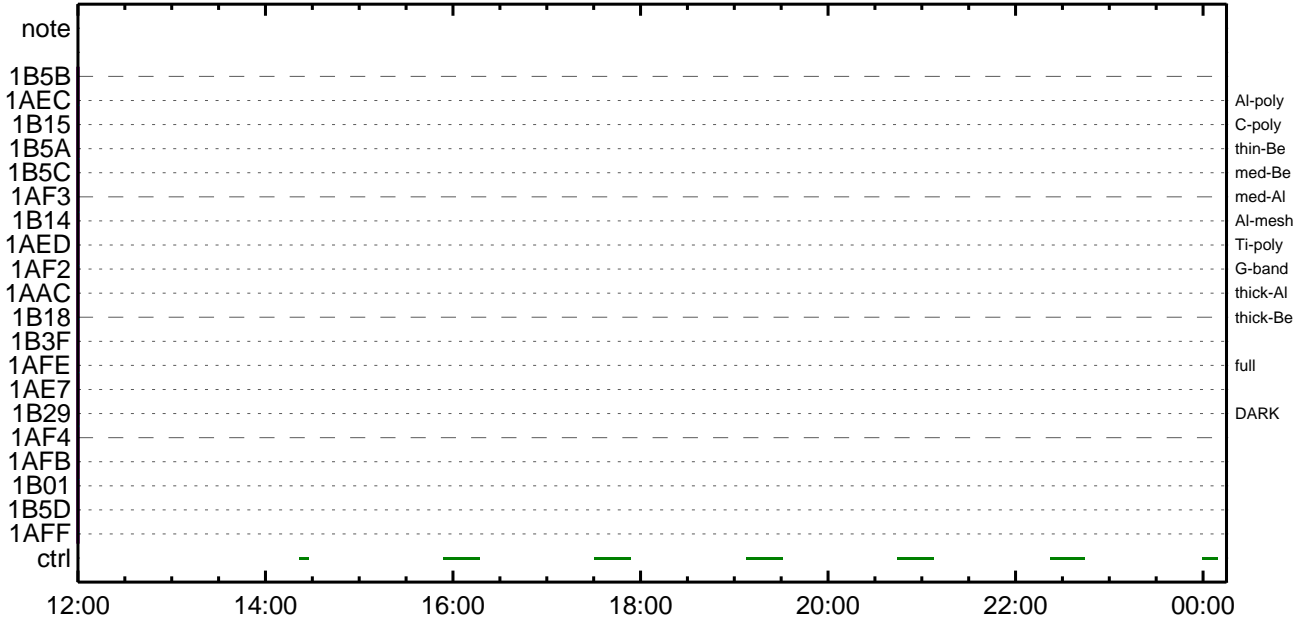
CMDI #0390 2016/12/16



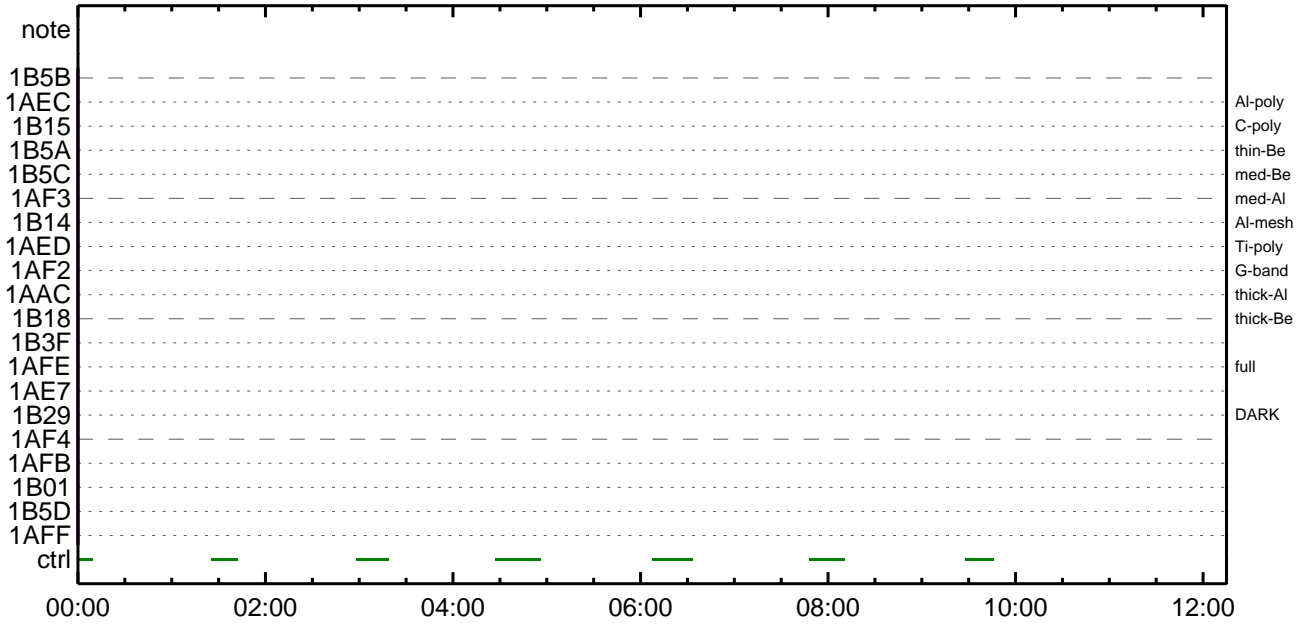
CMDI #0390 2016/12/17



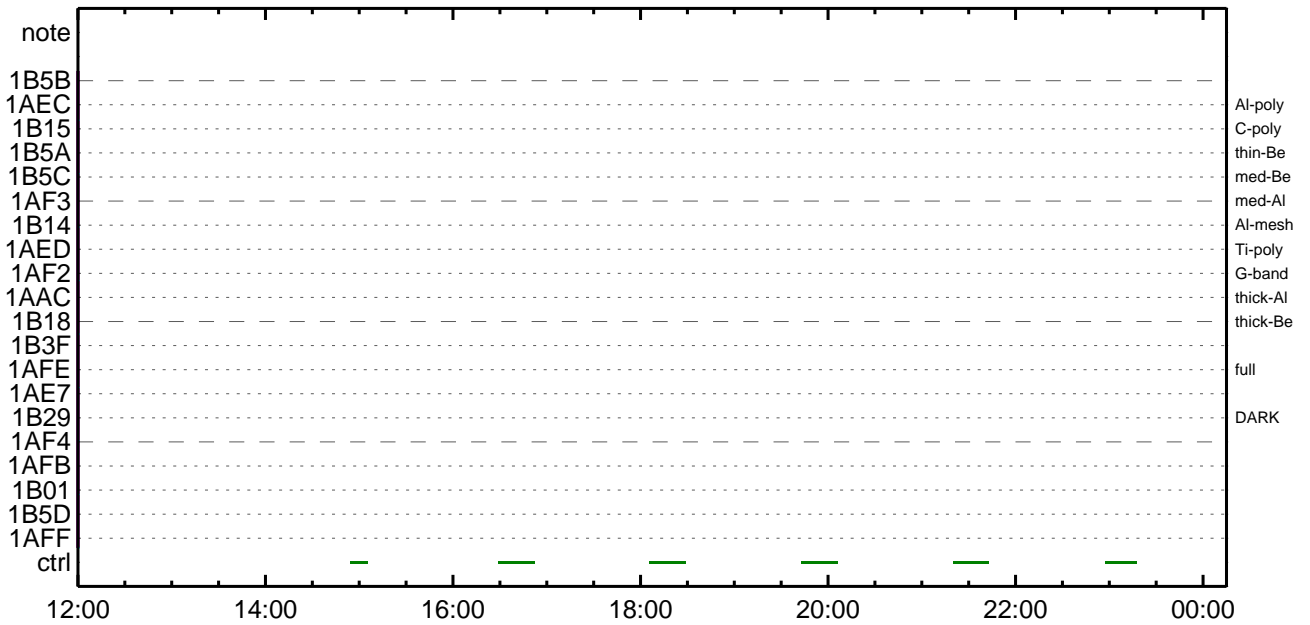
CMDI #0390 2016/12/17



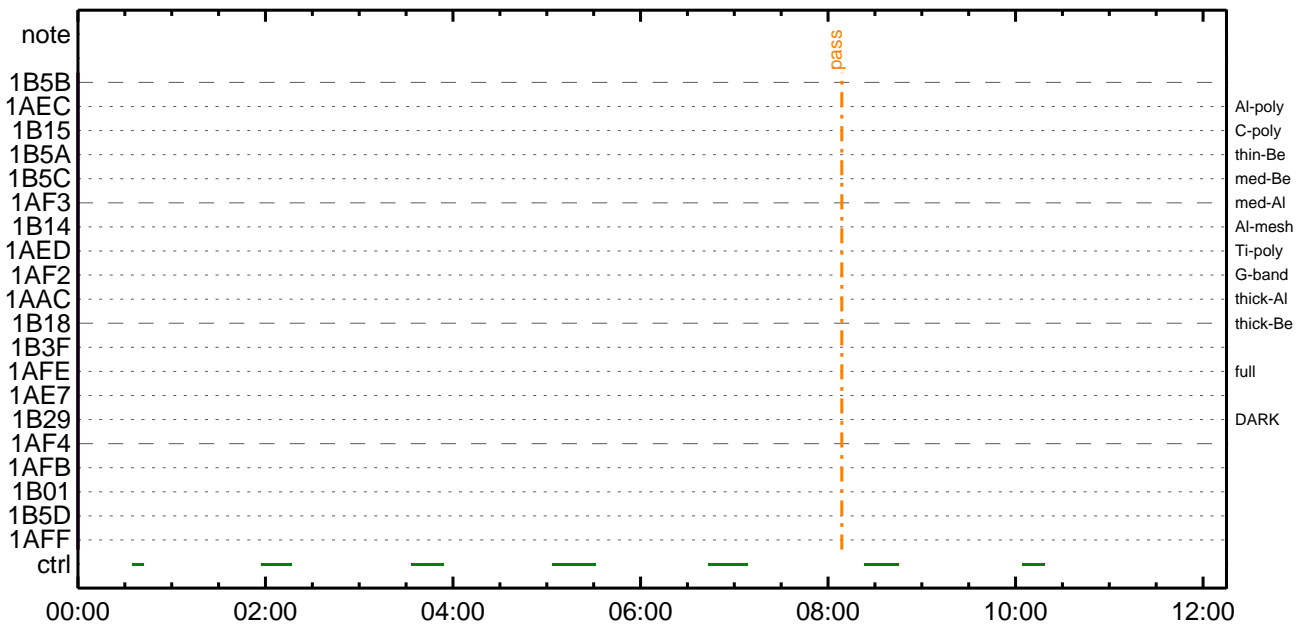
CMDI #0390 2016/12/18



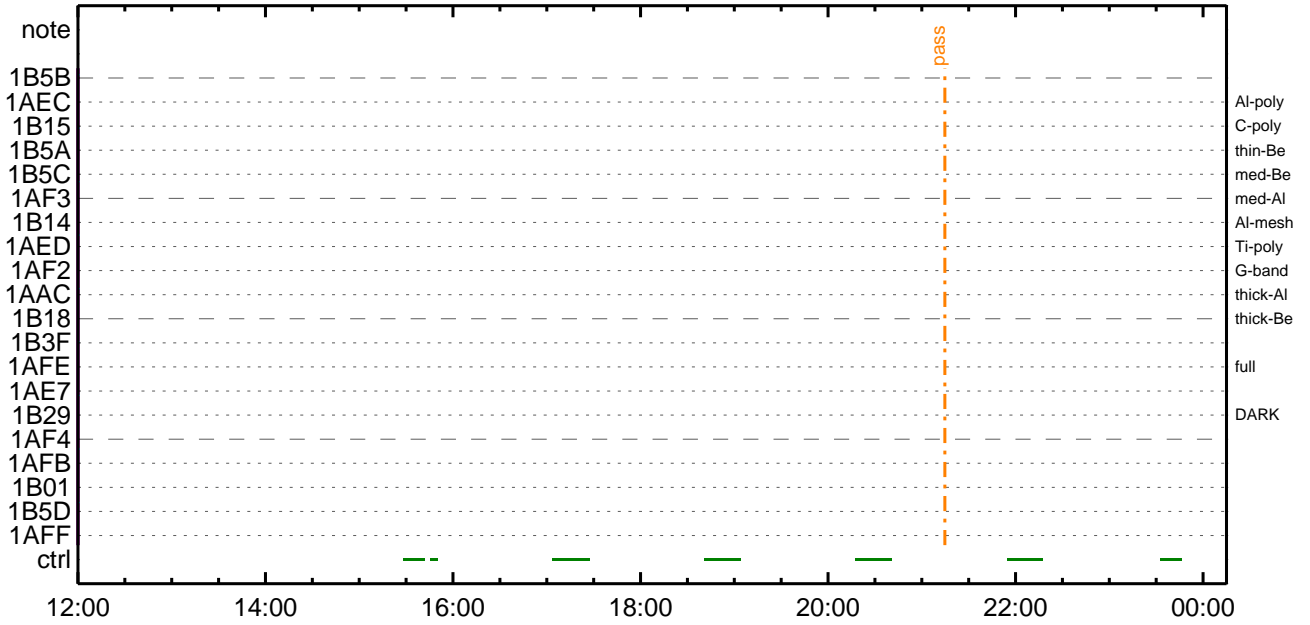
CMDI #0390 2016/12/18



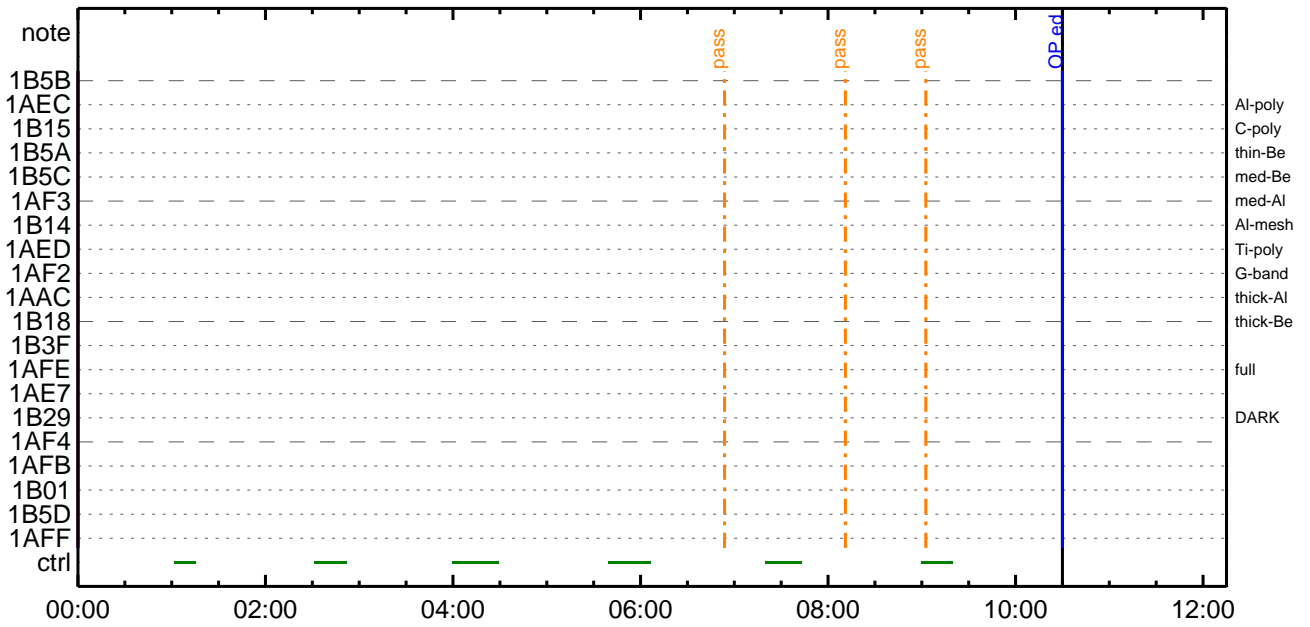
CMDI #0390 2016/12/19



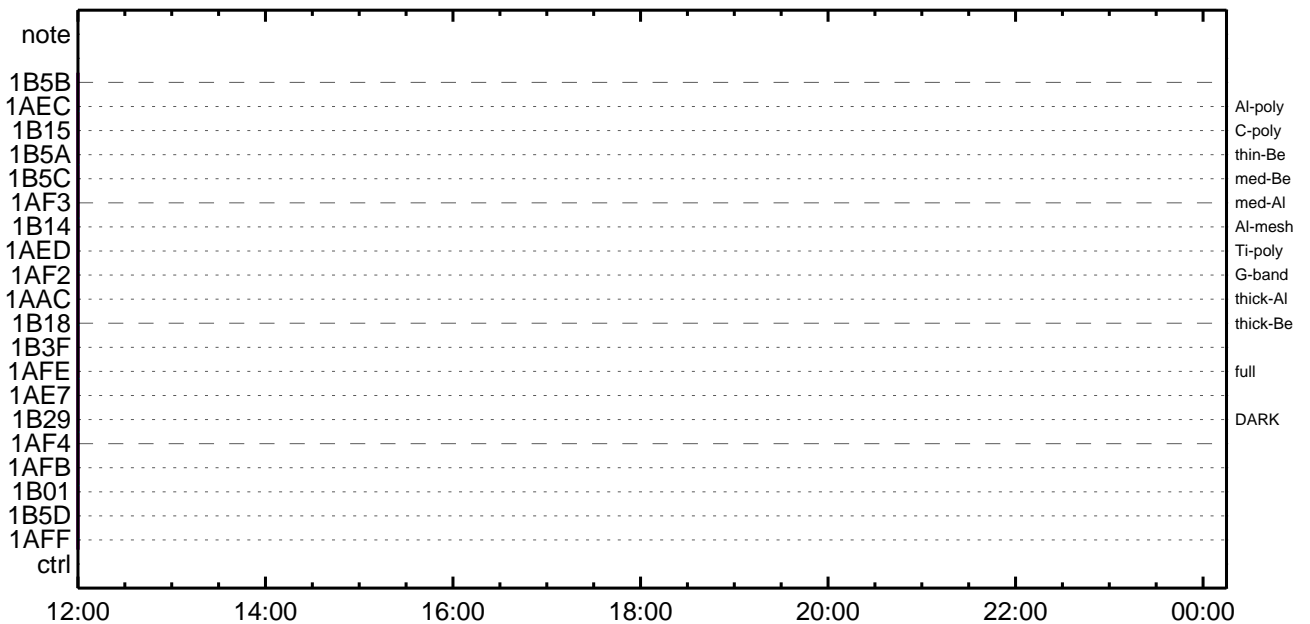
CMDI #0390 2016/12/19



CMDI #0390 2016/12/20



CMDI #0390 2016/12/20




```

0096 C.          0300; SET0EDUMP0I000iYNY10C100|030E; E
0097 C.
0098 . C. TIY3YBY0Y0E00dADi (UT)
0099 +. TI 2016-12-15 09:57:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          00[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2016-12-15 09:57:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          00[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2016-12-15 09:57:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          00[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2016-12-15 10:01:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          00[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. 0E2%0IAE%iIN0IYAY$YAY-1aIU
0116 C.          00[HK1_TI_CMD_ENA/DIS]        EQ          ENA
0117 C.          00[HK1_TI_CMD_NUM]          EQ          4
0118 C.          00[HK1_NEXT_EXEC_PIM]        EQ          DHU
0119 C.          00[HK1_NEXT_EXEC_DC]        EQ          0xB3
0120 C.
0121 . C. *****
0122 C. TI0I0EYAY0Yx
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; $ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          00[HK1_DMP_TOP_ADRS_1]        EQ          07
0129 C.          00[HK1_DMP_TOP_ADRS_0]        EQ          2B
0130 C.          00[HK1_DMP_BLOCK_NUM]        EQ          3
0131 C.          00[HK1_DMP_REPEAT_NUM]       EQ          0
0132 C.          00[HK1_DMA_DMP_PIM]         EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          00[HK1_PKT_FORM_NO]          EQ          7
0136 C.          00[HK1_PKT_GEN_TIME]         EQ          0.25 s
0137 C.          00[HK1_S_TLM_BIT_RATE]       EQ          32k
0138 C.          00[HK1_X_TLM_BIT_RATE]      EQ          4M
0139 C.          00[HK1_DMP_CHK_FLG]         EQ          EXEC
0140 C.
0141 . C. YAY0Yx%aI»0d3IC$
0142 C.          00[HK1_DMP_CHK_FLG]         EQ          NON
0143 C.
0144 . C. RAM ID=TI_TBL0I%E1C.e2IOK0d3IC$
0145 C.
0146 . C. DHUYa;Y%E;E%Y%,Yi;Y%E;E0dIa01
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          00[HK1_PKT_FORM_NO]          EQ          2
0150 C.          00[HK1_PKT_GEN_TIME]         EQ          0.5S
0151 C.          00[HK1_S_TLM_BIT_RATE]       EQ          32K
0152 C.          00[HK1_X_TLM_BIT_RATE]      EQ          4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2016-12-15 10:01:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2016-12-15 10:01:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC          (21 02)
0174 +. TI 2016-12-15 10:01:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC          (22)
0177 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2016-12-15 10:01:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP 0AI0I»0Y0EAD010EDCBC.x2e *****
0192 C. (%a0iY0YAYEYpYEYAYC%Y0E%0004A»U010e)
0193 . S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-304 2016-12-15 13:41:09 85 33 SOLAR-B MAIN //  
0001 C.  
0002 . C. ***** AOS *****  
0003 C.  
0004 . C. ;ãAOSYÃY$YÃY-¼Ä»Û;ã  
0005 C.  
0006 C. YÃYB;¼Y³YFÿYÉÄ+¿®  
0007 +. DC 00-00 NULL_DUMMY_CMD  
0008 C.  
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****  
0010 C. Āí;È¿¿ââ•µ°È»ÍxÃÇóÍYçYÃY×Yí;¼YÉ;ÈÈè¿µ•íÉ;ÈÈÈ¼°ÇÖâ•â¿¼í¹çâÍ;çÄ®, ùâ¹âèâBâçÄ+¿®â•âÈâââ³âÈ;f  
0011 +. DC 02-8E AOCU_ORB_UPD  
0012 C.  
0013 C.  
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****  
0015 C. Upload the Orbit Element and the Target Attitude  
0016 C. RAM-ID:TARGET_ATT  
0017 . S. RAM ram-150:TARGET_ATT  
0018 (  
0019 C.  
0020 C.  
0021 C. Set the dump memory area of TARGET_ATT  
0022 +. DC 02-48 AOCU_DUMP_SET  
0023 BC (07 00 00 00 18 00)  
0024 C.  
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]  
0026 C.  
0027 C.  
0028 C. Change the TLMFormatNo for the AOCs Dump Format  
0029 +. DC 01-22 DHU_MODE_CHNG  
0030 BC (04 0b f8)  
0031 C.  
0032 C. Wait for AOCSDUMP to end  
0033 C.  
0034 . C. Check the dump memory  
0035 C.  
0036 C. Result = OK [ ]  
0037 C.  
0038 +. DC 01-22 DHU_MODE_CHNG  
0039 BC (02 0a f8)  
0040 C.  
0041 C. <A_***>[TLM STS] FMT = 2 [ ]  
0042 C.  
0043 +. DC 02-8E AOCU_ORB_UPD  
0044 . C.  
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes  
0046 +. DC 07-FC EIS_MODE_CHG_ENA  
0047 BC (20)  
0048 . C. Verify EIS_MODE_CHG_FLG is ENA  
0049 +. DC 07-FC EIS_MODE_MANU  
0050 BC (21 02)  
0051 . C. Verify EIS in MANUAL mode  
0052 . C. Estimated OBSTBL upload time is 20s  
0053 C. *****  
0054 C. EIS START OBSTBL LOAD  
0055 C. *****  
0056 . S. RAM ram-820:EIS_OBSTBL  
0057 (  
0058 +. DC 07-FC EIS_DUMP_OBSTBL  
0059 BC (07 07 07 00 00 70 00)  
0060 C.  
0061 C. Execute, after the success of OBSTBL upload.  
0062 C. Set EIS TI-commands  
0063 +. TI 2016-12-15 10:01:50.0  
0064 DC 07-FC EIS_MODE_CHG_ENA  
0065 BC (20)  
0066 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP  
0067 C. *****  
0068 C. EIS END OBSTBL LOAD  
0069 C. *****  
0070 C.  
0071 . C. ***** MDP `ûÃîõí»õ¼YõÈÄð¹âèDCBC•x²è *****  
0072 C. (¼ã°îYÓYÃYÈYBpYÈYãYçYèè¼¼õ¼Ä»Ûâ¹âè)  
0073 . S. DC-BC dcbc-402:DCBC  
0074 (MDP_known_event)  
0075 C.  
0076 C.  
0077 . C. ***** YDÿ!•İ Daily±¿İÑâÈ`Øâ¹âèDCBC•x²è *****  
0078 . S. DC-BC dcbc-153:DCBC  
0079 (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0080 C.  
0081 C.  
0082 . C. ;ãLOS YÃY$YÃY-¼Ä»Û;ã  
0083 C.  
0084 . C. ***** LOS *****  
0085 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-305 2016-12-15 13:41:09 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÄYSYÄY-¼Ä»Û;ä
0005 C.
0006 C. YÄYB;¼Y³YFYÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ÄíË¿ðÄß•µ°Æ»Í×ÁÇüíYçYÄY×Yí;¼YÉjËÈÈµ•ííËjËßÈ¼°ÇÖß•ñ¿¼l¹çñÍjçÄ®, ùñ¹ñèñßñÇÄ+¿®ñ•ñÈñññ³ñÈj;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-284:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2016-12-15 10:01:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG_____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ( )
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR _____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 05 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 06 85 83 06 06)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 07 80 80 20 20)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 08 80 80 20 08)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 09 80 80 08 20)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0f 80 80 06 06)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 10 80 80 08 08)
0088 +. DC 07-F0 MDP_XRT_FLD_ENA
0089 BC (d8)
0090 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091 BC (c8)
0092 +. DC 07-F0 MDP_XRT_ARS_DIS
0093 BC (d5)
0094 +. DC 07-F0 MDP_XRT_AEC_RESET
0095 BC (d0)
```

0096 + DC 07-F0 MDP_XRT_FLD_RESET
0097 BC (da)
0098 + DC 07-F0 MDP_XRT_QT_PROG_SET
0099 BC (c4 08)
0100 + DC 07-F0 MDP_XRT_FL_PROG_SET
0101 BC (c5 07)
0102 . C. ----- Success Verify ? OK / NG ____
0103 C.
0104 C.
0105 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0106 C.
0107 +. DC 07-F0 MDP_XRT_MODE_OBSV
0108 BC (c2)
0109 +. TI 2016-12-15 10:01:02.0
0110 DC 07-F0 MDP_XRT_MODE_OBSV
0111 BC (c2)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C. ***** XRT END *****
0115 C.
0116 . C. ***** MDP 'úÃîâî»ö¼ÝðĚÂð¹ñēDCBC•x²è *****
0117 C. (%ã°îÿÓÿÄÿĚÿËÿĎÿŒÿŦÿŸÿÈÿÉÿÊÿËÿĹÿŔÿŪÿÜÿÝÿÞÿÿÿ)ÿ_ÿ
0118 . S. DC-BC dcbc-402:DCBC
0119 (MDP_known_event)
0120 C.
0121 C.
0122 . C. ***** ỲĐỲ¹.İ Daily±ĳİÑñĚ'Øñ¹ñēDCBC•x²è *****
0123 . S. DC-BC dcbc-153:DCBC
0124 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125 C.
0126 C.
0127 . C. ;ãLOSỲÁỲŞỲÄỲ⁻¼Á»Û;ă
0128 C.
0129 . C. ***** LOS *****
0130 C.

Dec 15, 16 13:43

XRT_OGLIST_0390.chk

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*** OP Sequence for XRT ***

2016/12/15	10:11:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/15	10:11:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/15	10:11:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/12/15	10:12:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	04 00 00 00 00		
2016/12/15	10:12:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/12/15	10:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/12/15	10:12:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/12/15	10:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/12/15	10:12:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/12/15	10:14:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09		
2016/12/15	10:14:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/12/15	10:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/12/15	11:02:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/15	11:02:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/15	11:02:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/12/15	11:02:06.0	XRT_PREFLR_STRT_414_OG [0x19e]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/12/15	11:02:30.0	XRT_Custom_430_OG [0x1ae]					
2016/12/15	11:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/12/15	11:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/12/15	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/15	12:29:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/15	12:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/12/15	12:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2016/12/15	12:30:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/12/15	12:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/12/15	12:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/12/15	12:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/12/15	12:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/12/15	12:32:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2016/12/15	12:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/12/15	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/12/15	14:46:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/15	14:46:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/15	14:46:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/12/15	14:46:36.0	XRT_PREFLR_STRT_414_OG [0x19e]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/12/15	14:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/12/15	14:57:00.0	XRT_Custom_430_OG [0x1ae]					
2016/12/15	14:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/12/15	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/15	15:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/15	15:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/12/15	16:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2016/12/15	16:00:18.0	XRT_FLD_DIS_406_OG [0x196]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/12/15	16:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/12/15	16:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/12/15	16:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2016/12/15	16:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					

2016/12/15	16:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/15	16:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/15	16:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/15	16:10:00.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/12/15	16:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00
2016/12/15	16:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/12/15	16:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/12/15	16:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/12/15	16:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/12/15	16:12:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/15	16:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2016/12/15	16:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/12/15	16:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/15	16:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/15	16:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/15	16:21:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/15	16:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/15	16:44:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/15	16:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/12/15	17:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/15	17:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/15	17:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/15	17:57:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/15	18:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/15	18:21:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/15	18:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/12/15	19:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/15	19:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/15	19:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/15	19:34:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/15	19:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/15	20:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/15	20:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2016/12/15	20:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/15	20:10:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/12/15	20:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
2016/12/15	20:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/12/15	20:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/12/15	20:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/12/15	20:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/12/15	20:12:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/15	20:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2016/12/15	20:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/12/15	21:12:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/15	21:12:03.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/15	21:12:05.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/15	21:12:07.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8

2016/12/15	21:15:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/12/15	21:35:01.0	XRT_Custom_430_OG [0x1ae]							
2016/12/15	21:36:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/15	22:49:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/15	22:49:03.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/15	22:49:05.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/15	22:49:07.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/12/15	22:52:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/12/15	23:10:01.0	XRT_Custom_430_OG [0x1ae]							
2016/12/15	23:11:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/16	00:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	00:26:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	00:26:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/16	00:26:36.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/12/16	00:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/12/16	00:34:00.0	XRT_Custom_430_OG [0x1ae]							
2016/12/16	00:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/16	01:50:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	01:50:03.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	01:50:05.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/16	01:50:07.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/12/16	01:53:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/12/16	02:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2016/12/16	02:10:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	02:10:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	02:10:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/12/16	02:10:24.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/12/16	02:10:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/12/16	02:10:28.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/12/16	02:10:30.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/12/16	02:10:32.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/16	02:13:02.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2016/12/16	02:13:04.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/12/16	02:13:06.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/16	03:25:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	03:25:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	03:25:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/16	03:25:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/12/16	03:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/12/16	03:46:00.0	XRT_Custom_430_OG [0x1ae]							
2016/12/16	03:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/16	04:55:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	04:55:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	04:55:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/16	04:55:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/12/16	04:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/12/16	05:23:00.0	XRT_Custom_430_OG [0x1ae]							
2016/12/16	05:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/16	06:05:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2016/12/16	06:05:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	06:05:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/12/16	06:06:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2016/12/16	06:06:18.0	XRT_FLD_DIS_406_OG [0x196] MDP_XRT_FLD_DIS	1	07-F0	d9
2016/12/16	06:08:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/12/16	06:08:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2016/12/16	06:08:58.0	XRT_QT_PROG_SET_442_OG [0x1ba] MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2016/12/16	06:09:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/16	06:35:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	06:35:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	06:35:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/16	06:35:36.0	XRT_PREFLR_STRT_414_OG [0x19e] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/16	06:36:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	01 00 00 00 00
2016/12/16	06:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/16	07:08:00.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	07:08:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	07:08:04.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/12/16	07:08:24.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2016/12/16	07:08:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/12/16	07:08:28.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2016/12/16	07:08:30.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2016/12/16	07:08:32.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/16	07:11:02.0	XRT_QT_PROG_SET_407_OG [0x197] MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2016/12/16	07:11:04.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/12/16	07:11:06.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/16	08:15:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	08:15:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	08:15:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/16	08:15:36.0	XRT_PREFLR_STRT_414_OG [0x19e] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/16	08:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/16	08:37:30.0	XRT_Custom_430_OG [0x1ae]			
2016/12/16	08:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/16	09:55:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	09:55:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	09:55:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/16	09:55:36.0	XRT_PREFLR_STRT_414_OG [0x19e] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/16	09:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/16	10:11:00.0	XRT_Custom_430_OG [0x1ae]			
2016/12/16	10:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/16	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	11:59:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	11:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/12/16	12:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b] AOCU_NM	5	02-76	02 00 00 00 00
2016/12/16	12:00:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2016/12/16	12:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/12/16	12:00:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2016/12/16	12:00:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2016/12/16	12:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]			

2016/12/16	12:02:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/16	12:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2016/12/16	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/12/16	15:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/16	15:20:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	15:20:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	15:20:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/16	15:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/12/16	15:42:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/12/16	15:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]							
2016/12/16	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2016/12/16	15:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/16	15:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	16:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	16:00:18.0	XRT_FLD_DIS_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2016/12/16	16:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	AOCS_Ore-point_Start_3_OG [0x099]							
2016/12/16	16:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	AOCU_NM	5	02-76	00	00	00	00	00
2016/12/16	16:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/12/16	16:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/12/16	16:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/12/16	16:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2016/12/16	16:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/16	16:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	16:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	16:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/12/16	16:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	AOCS_Ore-point_Start_1_OG [0x097]							
2016/12/16	16:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	AOCU_NM	5	02-76	04	00	00	00	00
2016/12/16	16:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/12/16	16:12:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/12/16	16:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/12/16	16:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/12/16	16:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/16	16:55:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2016/12/16	16:55:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/12/16	16:55:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/16	16:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	17:19:31.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	17:20:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/16	18:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/12/16	18:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/12/16	18:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_Custom_430_OG [0x1ae]							
2016/12/16	18:32:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2016/12/16	18:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/16	18:56:01.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	18:57:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/16	20:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/16	20:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/12/16	20:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2016/12/16	20:09:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/16	20:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]		5	02-76	01 00 00 00 00
			AOCU_NM			
2016/12/16	20:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/16	20:34:00.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	20:34:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	20:34:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/12/16	20:34:24.0	XRT_FLD_ENA_411_OG [0x19b]				
			MDP_XRT_FLD_ENA	1	07-F0	d8
2016/12/16	20:34:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/12/16	20:34:28.0	XRT_AEC_RESET_448_OG [0x1c0]				
			MDP_XRT_AEC_RESET	1	07-F0	d0
2016/12/16	20:34:30.0	XRT_ARS_DIS_423_OG [0x1a7]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2016/12/16	20:34:32.0	XRT_FLD_RESET_433_OG [0x1b1]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/16	20:37:02.0	XRT_QT_PROG_SET_407_OG [0x197]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2016/12/16	20:37:04.0	XRT_FL_PROG_SET_436_OG [0x1b4]				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/12/16	20:37:06.0	XRT_CTRL_AUTO_408_OG [0x198]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/16	21:47:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	21:47:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	21:47:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/16	21:47:06.0	XRT_PREFLR_STRT_414_OG [0x19e]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/16	21:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/16	22:09:30.0	XRT_Custom_430_OG [0x1ae]				
2016/12/16	22:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/16	23:24:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	23:24:32.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/16	23:24:34.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/16	23:24:36.0	XRT_PREFLR_STRT_414_OG [0x19e]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/16	23:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/16	23:40:00.0	XRT_Custom_430_OG [0x1ae]				
2016/12/16	23:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/17	00:56:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/17	00:56:32.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/17	00:56:34.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/17	00:56:36.0	XRT_PREFLR_STRT_414_OG [0x19e]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/17	00:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/17	01:08:00.0	XRT_Custom_430_OG [0x1ae]				
2016/12/17	01:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/17	02:23:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/17	02:23:32.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/17	02:23:34.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/17	02:23:36.0	XRT_PREFLR_STRT_414_OG [0x19e]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/17	02:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/17	02:44:00.0	XRT_Custom_430_OG [0x1ae]				
2016/12/17	02:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/17	03:52:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/17	03:52:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/17	03:52:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/17	03:52:06.0	XRT_PREFLR_STRT_414_OG [0x19e]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/17	03:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/17	04:21:00.0	XRT_Custom_430_OG [0x1ae]				
2016/12/17	04:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				

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2016/12/17	05:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/17	05:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/17	05:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/17	05:31:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/12/17	05:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/12/17	05:58:30.0	XRT_Custom_430_OG [0x1ae]								
2016/12/17	05:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/17	06:07:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/17	06:07:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/17	06:07:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/12/17	06:08:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				
2016/12/17	06:08:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/12/17	06:10:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/12/17	06:10:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/12/17	06:10:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2016/12/17	06:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/17	06:17:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/17	06:17:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/17	06:17:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/12/17	06:18:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00				
2016/12/17	06:18:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/12/17	06:18:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/12/17	06:18:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/12/17	06:18:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/12/17	06:18:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/17	06:20:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2016/12/17	06:20:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/12/17	06:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/17	07:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/17	07:11:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/17	07:11:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/17	07:11:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/12/17	07:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/12/17	07:35:30.0	XRT_Custom_430_OG [0x1ae]								
2016/12/17	07:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/17	08:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/17	08:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/17	08:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/17	08:51:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/12/17	08:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/12/17	09:12:00.0	XRT_Custom_430_OG [0x1ae]								
2016/12/17	09:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/17	10:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/17	10:32:30.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/17	10:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/17	10:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/17	10:32:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/12/17	10:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								

2016/12/17	11:05:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			AOCU_NM	5	02-76	00 00 00 00 00